In the Claims:

Please cancel claims 7 and 8.

Please amend claim 6 as follows:

6. (Amended) A power amplifier system for driving a load comprising:

a pulse width modulation power circuit creating fipple spectra;

an error amplifier and modulator circuit connected to an input of the pulse width modulation circuit;

a demodulation filter connected between said pulse width modulation power circuit and the load;

a feedback control loop coupled to said pulse width modulation power circuit and including:

an active low-pass filter;

a first resistive voltage divider circuit coupled between the output of said demodulation filter and a first input of said low-pass filter;

a feedback demodulation filter coupled to a second input of said low-pass filter and including at least one isolated-integrator band-reject filter; and

a second resistive voltage divider circuit coupled between the output of said pulse width modulation power circuit and said feedback demodulation filter.

REMARKS

Claims 1-6 are at issue. No claims have been allowed. Claims 7 and 8 have been cancelled.

Applicant has amended the specification by taking language which was deleted from the drawings, as filed, and adding that language to the specification. No new matter has been added.

The Examiner has objected to the circuits of Figure 2 and has indicated that the circuits needs to be labeled separately since each is a separate circuit. Applicant submits that the circuits are separately labeled as Figures 2a, 2b, and 2c which is consistent with the specification.

The Applicant has requested the Examiner's approval for amending the drawings by deleting certain language from the drawings and by adding numerals and lead lines to the drawings. No new matter has been added.

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